Breaking Ciphers

with Special Purpose Hardware



PC's = slow...

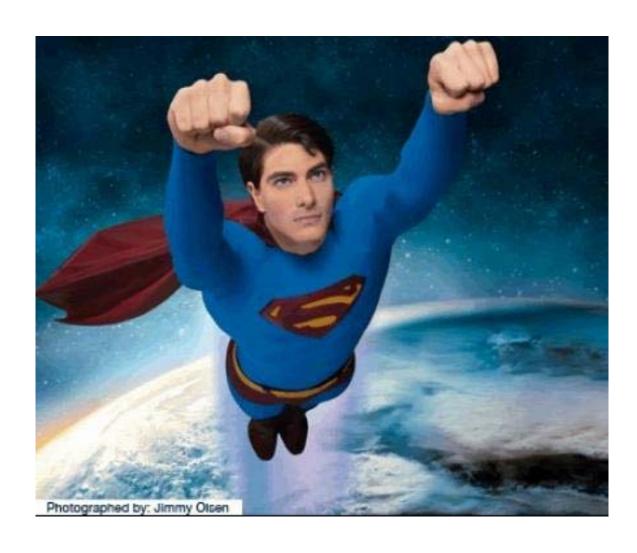






POWER!







Steal Secrets & Break Ciphers!





New Invention!



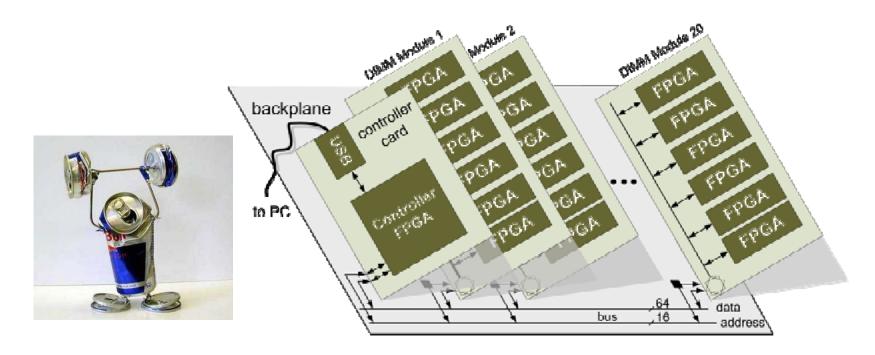


- very fast hardware
- cost-optimized!
- · parallelized!



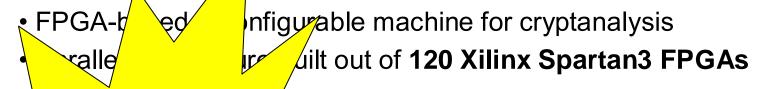


- FPGA-based reconfigurable machine for cryptanalysis
- Parallel architecture built out of 120 Xilinx Spartan3 FPGAs
- Modular design:
 - Backplane with FPGA modules (each with 6 low-cost FPGAs)
 - Controller card with USB interface or TCP/IP Interface

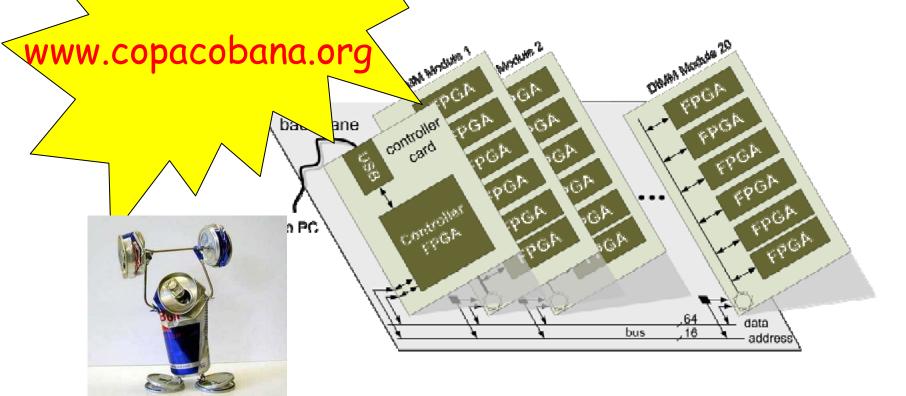


Special Purpoge Hardware





available from Amodules (each with 6 low-cost FPGAs)
USB interface or TCP/IP Interface







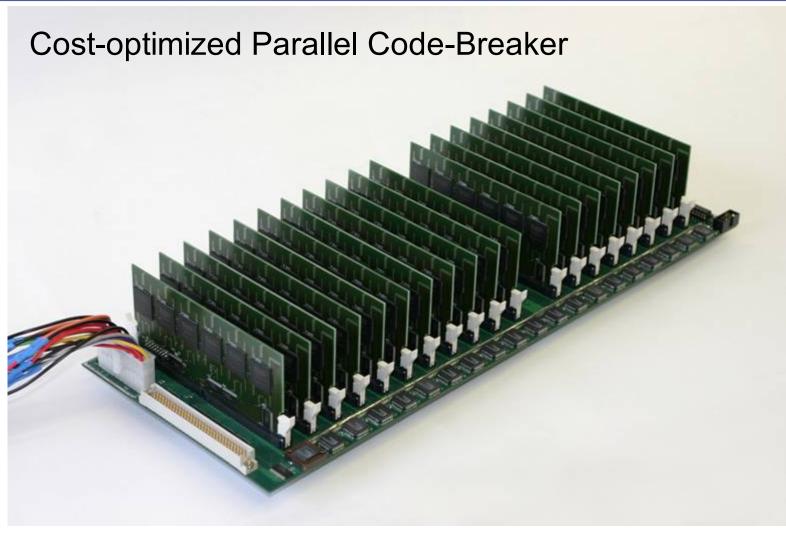


... Easy to remember: Copacobana...



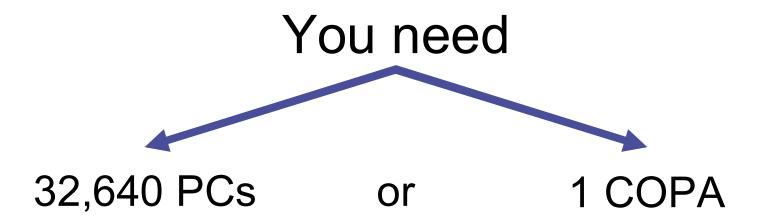


COPACOBANA: FPGA Modules





To break DES in 6.4 days in average







GREENCRYPT



Consumption-performance ratio of DES¹⁾: PC vs. FPGA

consumption per the average DES brute-force attack



Pentium4@3GHz:

≈ 750 MWh



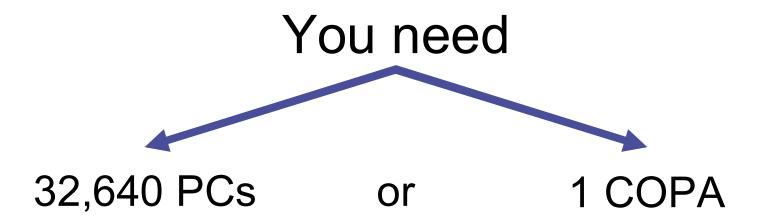
Xilinx XC3S1000@136MHz: \approx 92 kWh

➤ Consumtion-performance ratio differs by 3-4 orders of magnitude!

¹⁾ Based on actual optimized implementations



To break DES in 6.4 days in average





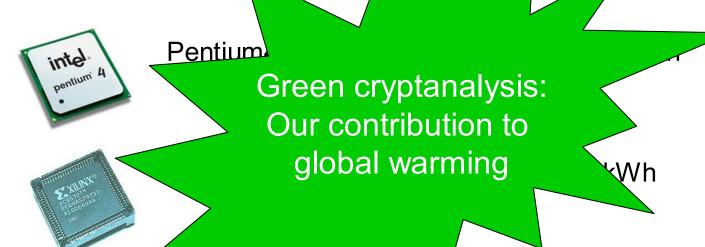


GREENCRYPT



Consumption-performance ratio of DES1: PC vs. FPGA

consumption per the average some orce at ack



► Consumtion performance ratio differs by 3-4 orders of magnitude!

¹⁾ Based on actual optimized implementations

Play NSA





9 of 10 spies recommend COPACOBANA



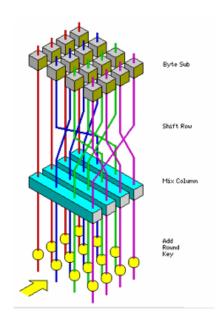


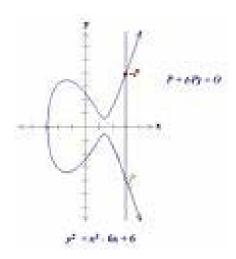
It is POWERFUL















Break electronic Passports!



Very cool: Steal Identities! Track People!





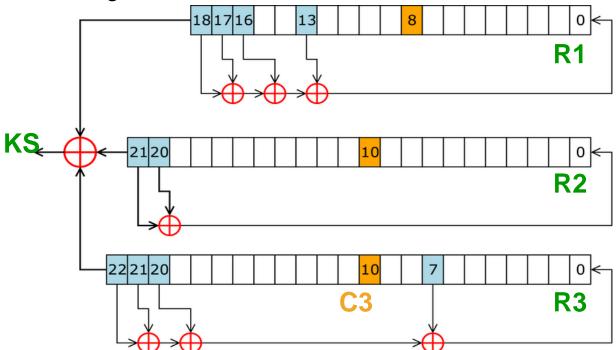
Guess complete content of R1, R2

Derive content of R3 step-by-step:

- a. Derive **MSB** of **R3** from **R1**, **R2**, and known **KS**
- b. Guess C3 (clocking bit of R3) until R3 is completely determined.

Continue clocking A5/1 & compare generated KS against known KS

If 64 bits of generated KS match, then CANDIDATE FOUND



Have (il)legal fun!

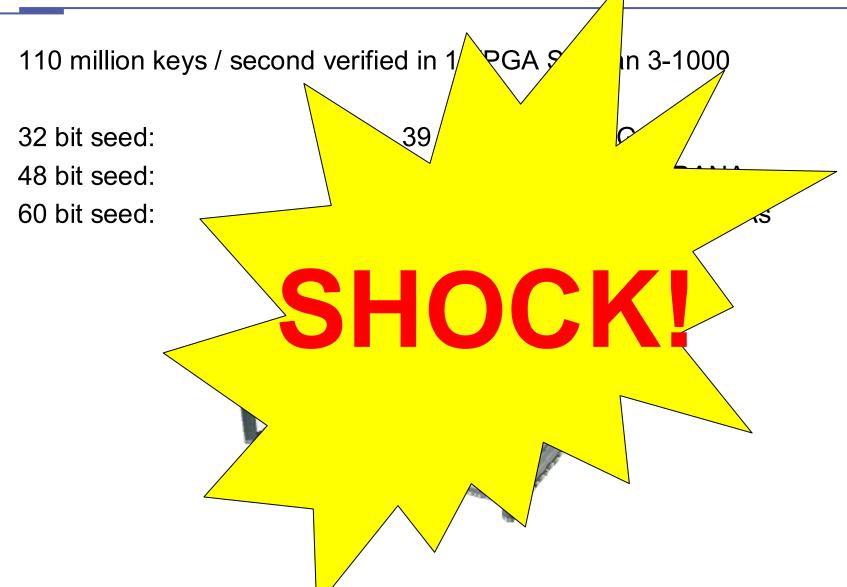




Beamter beim Lauschen mit einem Richtmikrofon









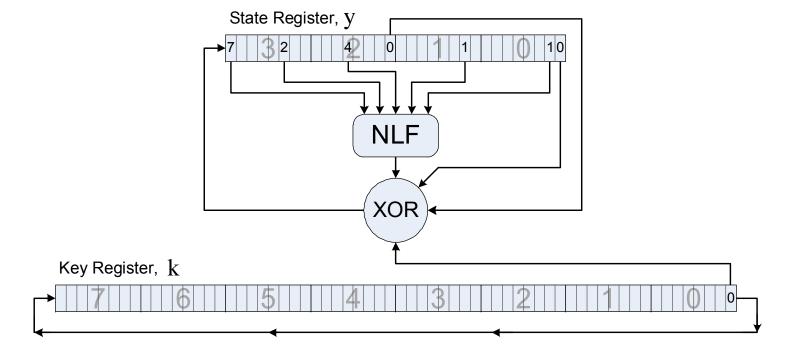
Break KeeLoq with COPACOBANA!

110 million keys / second verified in 1 FPGA Spartan 3-1000

32 bit seed: 39 seconds / 1 FPGA

48 bit seed: 5.9 hours / 1 COPACOBANA

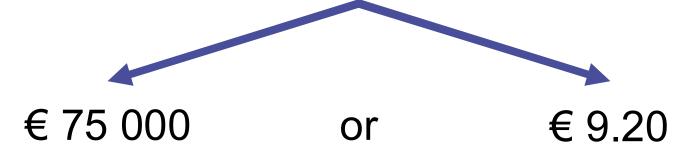
60 bit seed: 101 days / 10 COPACOBANAs



To break DES



You pay for electricity













9 of 10 economists recommend COPACOBANA











... Easy to remember: Copacobana...



COPACOBANA!

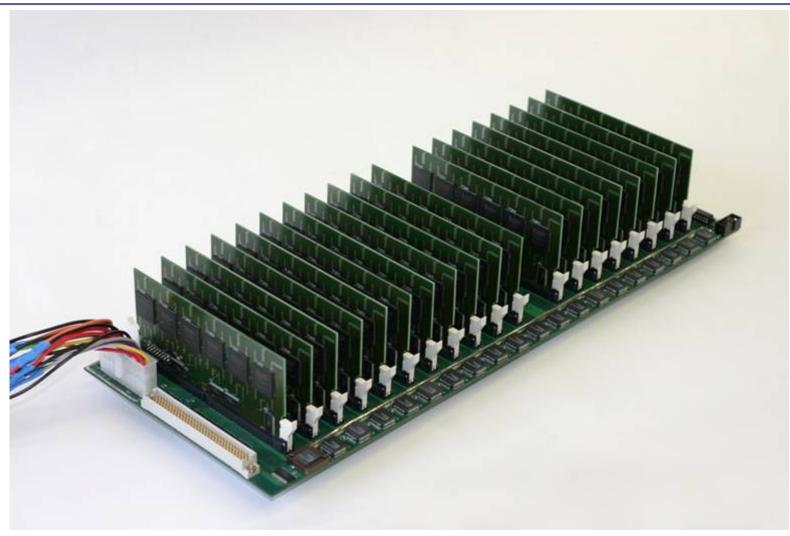




► COPACOBANA

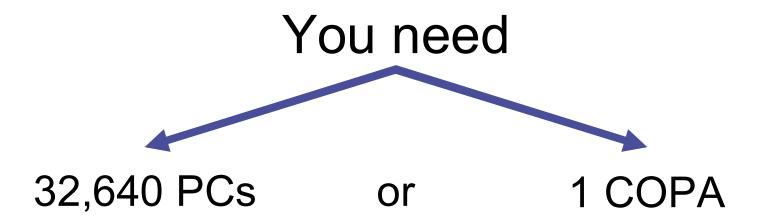


COPACOBANA: FPGA Modules





To break DES in 6.4 days in average



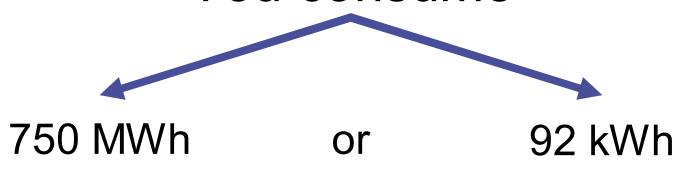




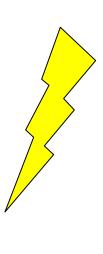
To break DES



You consume









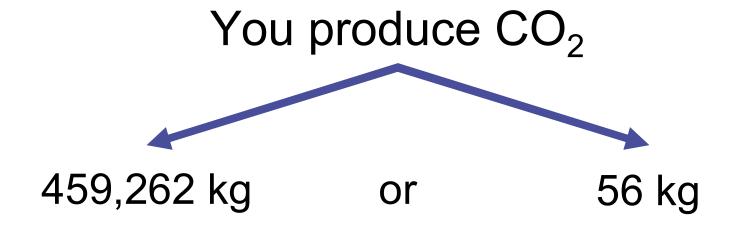
9 of 10 environmentalists recommend COPACOBANA



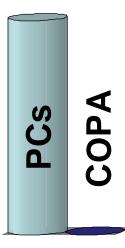


To break DES











Play NSA





SLOW!







Fastest Machine of the Summer!

